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	Application No. 09/898,986	
Applicant Yamazaki, et al.		
Filing Date July 3, 2001	Group Art Unit 2813	
	O7977-163003 Applicant Yamazaki, et al. Filing Date	

MADEN			U.S. Pate	ent Documents			
Examin Initial		Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
/ Well	AA	5,898,204	4/1999	Watanabe			
1	AB	5,998,854	12/1999	Morishita et al.			
	AC	6,180,957	1/2001	Miyasaka et al.			
	AD	5,550,397	08/27/96	Lifshitz et al.	T		
	AE	5,552,624	09/03/96	Shotnicki et al.			
	AF	5,616,935	04/01/97	Koyama et al.			
	AG	5,659,192	08/19/97	Sarma et al.			
NU	∠ AH	5,726,459	03/10/98	Hsu et al.			
	AI			ii:			

Foreign Patent Documents or Published Foreign Patent Applications								
	Desig.	Document	Publication	Country or				lation
	ID ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AJ	04-206971	07/28/92	JAPAN .				
	AK	04-286339	10/12/92	JAPAN				
	AL	06-232059	08/19/94	JAPAN			C	-11
1	AM	07-169974	07/04/95	JAPAN			ایب ۳	西
	AN	07-176753	07/14/95	JAPAN		ij	<u>g</u> –	EN
W	AO	07/321339	12/08/95	JAPAN		ţ	CET	
	miner itial	miner Desig. ID AJ AK AL AM AN	Desig. Document Number AJ 04-206971 AK 04-286339 AL 06-232059 AM 07-169974 AN 07-176753	Desig. Document Publication Date	Desig. Document Publication Country or Patent Office AJ 04-206971 07/28/92 JAPAN AK 04-286339 10/12/92 JAPAN AL 06-232059 08/19/94 JAPAN AM 07-169974 07/04/95 JAPAN AN 07-176753 07/14/95 JAPAN	Desig. Document Publication Country or Patent Office Class	Design Document Publication Country or Patent Office Class Subclass	Desig. Document Publication Patent Office Class Subclass Yes

	Other Documents (include Author, Title, Date, and Place of Publication)						
Examiner							
Initial	1D	Document f. O					
Sh	ΑP	Wang et al., Enhanced Performance of Accumulation Mode 0.5 µm CMOS/SOI Operated at 300 K and 85 K, IEEE, IEDM 91, pp. 679-682.					
	AQ	Fossum et al., "Anomalous Leadage Current in LPCVD Polysilicon MOSFET's", September 1995, IEEE Transactions on Electron Devices, Vol. ED-32, No. 9; pp. 1878-1884.					
	AR	Qian et al., "Inversion/Accumulation-Mode Polysilicon Thin-Film Transistors: Characterization and Unified Molding", September 1988, IEEE Transactions on Electron Devices, Vol. 35, pp. 1501-1509.					
XU	AS	Malhi et al., "p-Channel MOSFET's in LPCVD Polysilicon", October 1983, IEEE Electron Device Letters, Vol. EDL-4, No. 10, pp. 369-371.					

Examiner Signature			Date Considered
Lhours Solu	LE)	10/28/05
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			Substitute Disclosure Form (PTO-1449)